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APPLICATION NO.	FILING	G DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/990,474	11/2	1/2001	Yuji Judai	MTS-2700US1 3161		
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Ratner & Prestia				EXAM	EXAMINER	
P.O. Box 980 Valley Forge, PA 19482			WEISS, H	WEISS, HOWARD		
				ART UNIT	PAPER NUMBER	
				2814		
			DATE MAILED: 07/18/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 07-01)

	/						
	Application No.	Applicant(s)					
	09/990,474	JUDAI, YUJI					
Office Action Summary	Examin r	Art Unit					
TI 4111 NO DATE 4111	Howard Weiss	2814					
Th MAILING DATE of this communication app Period for Reply	ars on the cover sneet with the	n correspond nce address					
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period with Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	6(a). In no event, however, may a reply l within the statutory minimum of thirty (30 ill apply and will expire SIX (6) MONTHS cause the application to become ABAND	pe timely filed) days will be considered timely. from the mailing date of this communication. ONED (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on 14 M	<u>lay 2003</u> .						
2a)⊠ This action is FINAL . 2b)□ This	s action is non-final.						
3) Since this application is in condition for allowal closed in accordance with the practice under E							
Disposition of Claims	Ex parte Quayle, 1955 C.D. 1	1, 433 O.G. 213.					
4)⊠ Claim(s) <u>5,11,12,14 and 16-21</u> ks/are pending i	n the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>5,11,12,14 and 16-21</u> h s/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examiner		Evaminer					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11) The proposed drawing correction filed on							
If approved, corrected drawings are required in rep							
12) The oath or declaration is objected to by the Examiner.							
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) ☐ All b) ☐ Some * c) ☐ None of:							
1. Certified copies of the priority documents	have been received.						
2. Certified copies of the priority documents have been received in Application No							
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
14) Acknowledgment is made of a claim for domestic	•						
a) ☐ The translation of the foreign language pro-							
Attachment(s)							
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) S. Patent and Tradement Office.	5) Notice of Infor	mary (PTO-413) Paper No(s) mal Patent Application (PTO-152)					

Attorney's Docket Number: MTS-2700US1

Filing Date: 10/22/98

Continuing Data: Division of 09/177,038 (10/22/98, now abandoned)

Claimed Foreign Priority Date: 10/24/97 (JPX)

Applicant(s): Judai

Examiner: Howard Weiss

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 11 and 16 to 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Arita et al. (U.S. Patent No. 5,624,864) and Bailey (U.S. Patent No. 6,249,014).

Arita et al. show most aspects of the instant invention (e.g. Figure 20) including:

- > providing a circuit board 31 and forming a first insulating film 37 at least indirectly on said circuit board
- ➢ forming a ferroelectric capacitor 41 by forming a lower electrode 38, a ferroelectric film 39 and an upper electrode 40 on said first insulating film
- > forming a second insulating film 46 and forming a plurality of contact openings 43b, 43c to said upper and lower electrodes
- forming a metal wiring pattern in said openings including a base layer 54b, 54c of TiN and an upper layer 44b, 44c of Al
- > forming a surface protective film **55** of SiN over said second insulating film and metal wiring pattern

Arita et al. do not show heating-treating the TiN layer before depositing the upper Al layer. Bailey teaches (e.g. Figures 7) to heating-treat (i.e. anneal in step 838) the



TiN layer before depositing the Al upper layer (step **844**) to protect the ferroelectric capacitor from hydrogen and moisture during subsequent process steps (Column 4 Lines 18 to 22). It would have been obvious to a person of ordinary skill in the art at the time of invention to heating-treat the TiN layer before depositing the Al upper layer as taught by Bailey in the process of Arita et al. to protect the ferroelectric capacitor from hydrogen and moisture during subsequent process steps.

3. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Arita et al. and Bailey, as applied to Claim 16 above, and in further view of Wolf et al. (1986).

Arita et al. and Bailey show most aspects of the instant invention (Paragraph 2) except for deposing the Al layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C. Wolf et al. teach to heat circuit boards during sputtering of Al to improve film properties (third paragraph from top of page 367). It would have been obvious to a person of ordinary skill in the art at the time of invention to heat circuit boards during sputtering of Al as taught by Wolf et al. in the process of Arita et al. and Bailey to improve film properties.

Further, it would have been obvious to one having ordinary skill in the art at the time the invention was made to heat the circuit board to a temperature rang of 100 to 400° C, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller, 105 USPQ 233*.

4. Claims 5, 11 and 16 to 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Patel et al. (U.S. Patent No. 5,374,578) and Bailey.

Patel et al. show most aspects of the instant invention (e.g. Figures 1 to 11) including:

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providing a circuit board 1 and forming a first insulating film 10 at least indirectly on said circuit board

- > forming a ferroelectric capacitor by forming a lower electrode 12, a ferroelectric film 14 and an upper electrode 16 on said first insulating film
- → forming a second insulating film 18 using TEOS-CVD method utilizing TEOS
 activated by O₃ (Column 5 Lines 19 to 32)
- > forming a plurality of contact openings 20, 22 to said upper and lower electrodes
- ➢ forming a metal wiring pattern 26 in said openings including a base layer of TiN and an upper layer of AI (Column 5 Lines 47 to 58)
- ➤ heat treating the TiN layer in the temperature range between 200 to 650° C (Column 5 Lines 64 to 68).
- > forming a surface protective film 28 over said second insulating film and metal wiring pattern

Patel et al. do not show heating-treating the TiN layer before depositing the upper Al layer. Bailey teaches (e.g. Figures 7) to heating-treat (i.e. anneal in step 838) the TiN layer before depositing the Al upper layer (step 844) to protect the ferroelectric capacitor from hydrogen and moisture during subsequent process steps (Column 4 Lines 18 to 22). It would have been obvious to a person of ordinary skill in the art at the time of invention to heating-treat the TiN layer before depositing the Al upper layer as taught by Bailey in the process of Patel et al. to protect the ferroelectric capacitor from hydrogen and moisture during subsequent process steps.

5. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Arita et al. and Bailey, as applied to Claim 16 above, and in further view of Wolf et al. (1986).

Arita et al. and Bailey show most aspects of the instant invention (Paragraph 2) except for depositing the SiN layer using PECVD at RF power 300 W or less. Wolf et al. teach to deposit the SiN layer using PECVD so as to use low temperatures (Middle paragraph of page 192). It would have been obvious to a person of ordinary



skill in the art at the time of invention to deposit the SiN layer using PECVD as taught by Wolf et al. in the process of Arita et al. and Bailey so as to use low temperatures.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to use RF power of 300 W or less, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller, 105 USPQ 233.*

6. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Patel et al. and Bailey, as applied to Claim 16 above, and in further view of Wolf et al.

Patel et al. and Bailey show most aspects of the instant invention (Paragraph 4) except for deposing the Al layer using sputtering and heating the circuit board in a temperature rang of 100 to 400° C. Wolf et al. teach to heat circuit boards during sputtering of Al to improve film properties (third paragraph from top of page 367). It would have been obvious to a person of ordinary skill in the art at the time of invention to heat circuit boards during sputtering of Al as taught by Wolf et al. in the Process of Patel et al. and Bailey to improve film properties.

It would have been obvious to one having ordinary skill in the art at the time the invention was made to heat the circuit board to a temperature rang of 100 to 400° C, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller, 105 USPQ 233.*

Response to Arguments

7. Applicant's arguments with respect to Claims 5, 11, 12, 14 and 16 to 21 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

- 9. Papers related to this application may be submitted directly to Art Unit 2814 by facsimile transmission. Papers should be faxed to Art Unit 2814 via the Art Unit 2814 Fax Center located in Crystal Plaza 4, room 3C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (15 November 1989). The Art Unit 2814 Fax Center number is (703) 308-7722 or -7724. The Art Unit 2814 Fax Center is to be used only for papers related to Art Unit 2814 applications. The official TC2800 Before-Final, (703) 872-9318, and After-Final, (703) 872-9319, Fax numbers will provide the fax sender with an auto-reply fax verifying receipt of their fax by the USPTO.
- 10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Howard Weiss at **(703) 308-4840** and between the hours of 8:00 AM to 4:00 PM (Eastern Standard Time) Monday through Friday or by e-mail via **Howard.Weiss@uspto.gov**.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group 2800 Receptionist at **(703) 308-0956**.

11. The following list is the Examiner's field of search for the present Office Action:

Field of Search	Date
U.S. Class / Subclass(es): 438/ 3, 240	thru 7/11/03
Other Documentation: none	
Electronic Database(s): EAST	thru 7/11/03

HW/hw 11 July 2003 Howard Weiss Examiner Art Unit 2814

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